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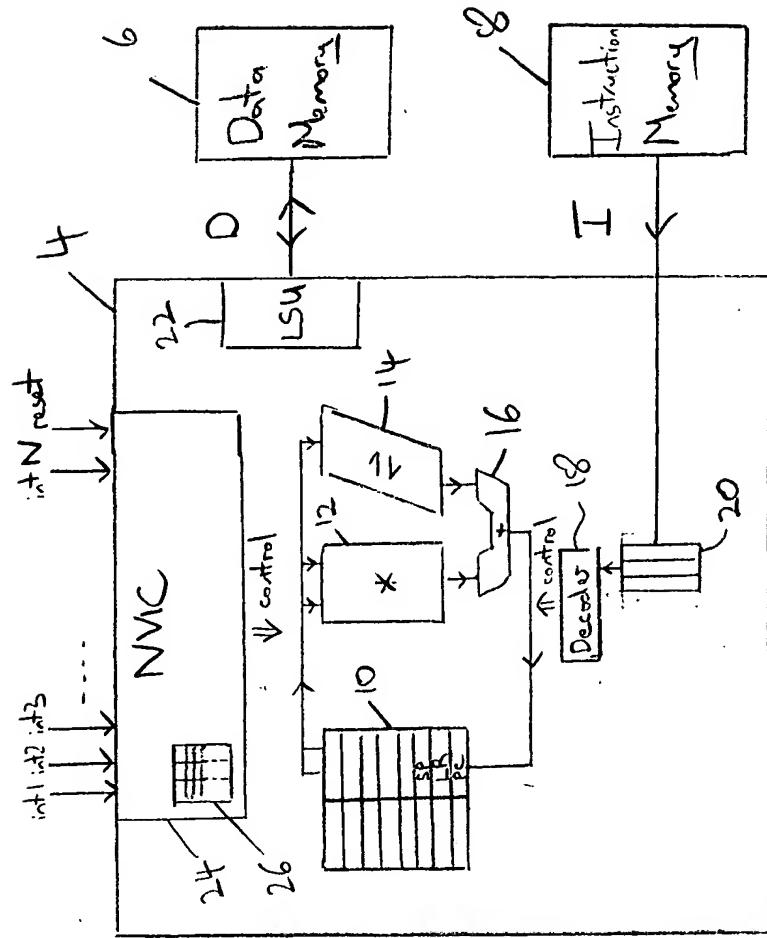


Fig. 1

NVIC Control Table

	Pending	Active	Priority pre-emption	Priority ordering
Int 0		1		↔
Int 1				↓
Int 2	1			↓
Int 3		1		↓
⋮				

Fig. 2

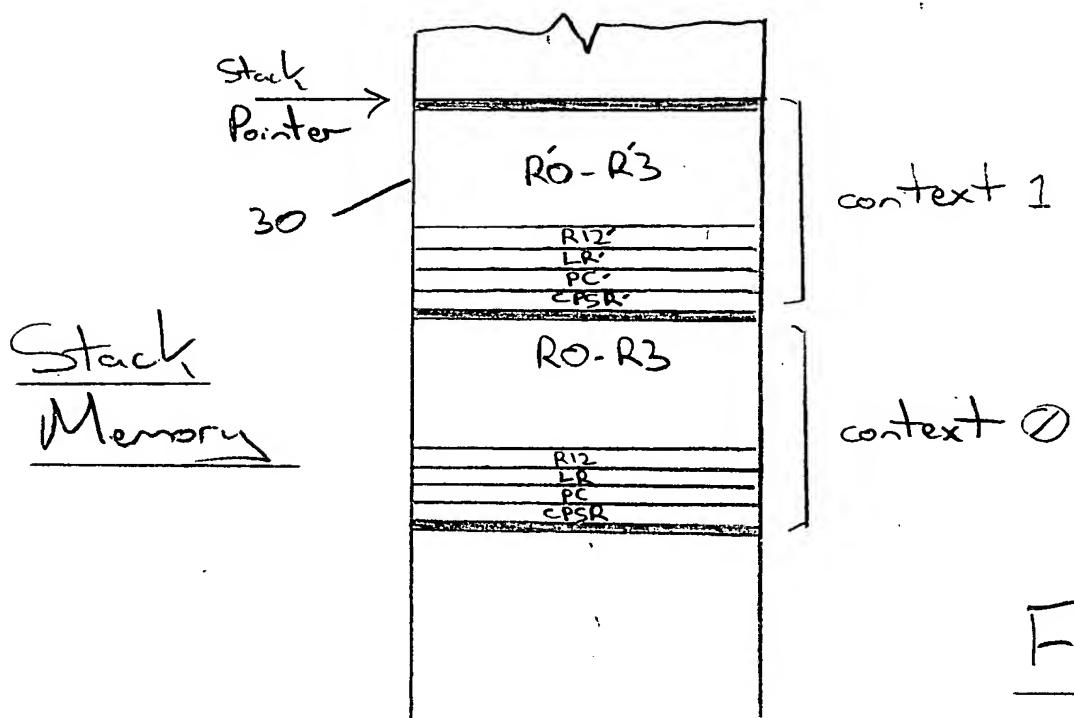


Fig. 3

Priority Inversions

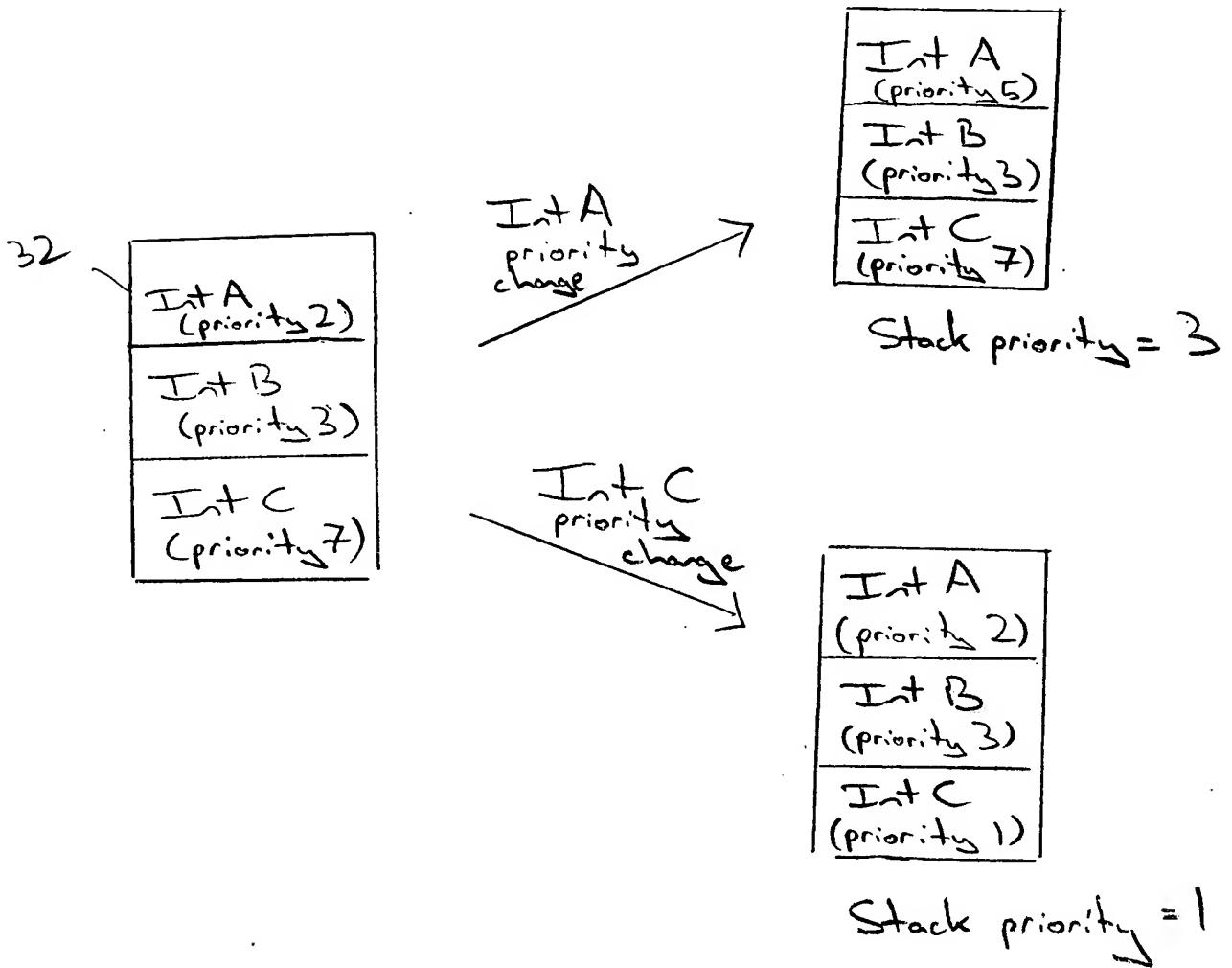


Fig. 4

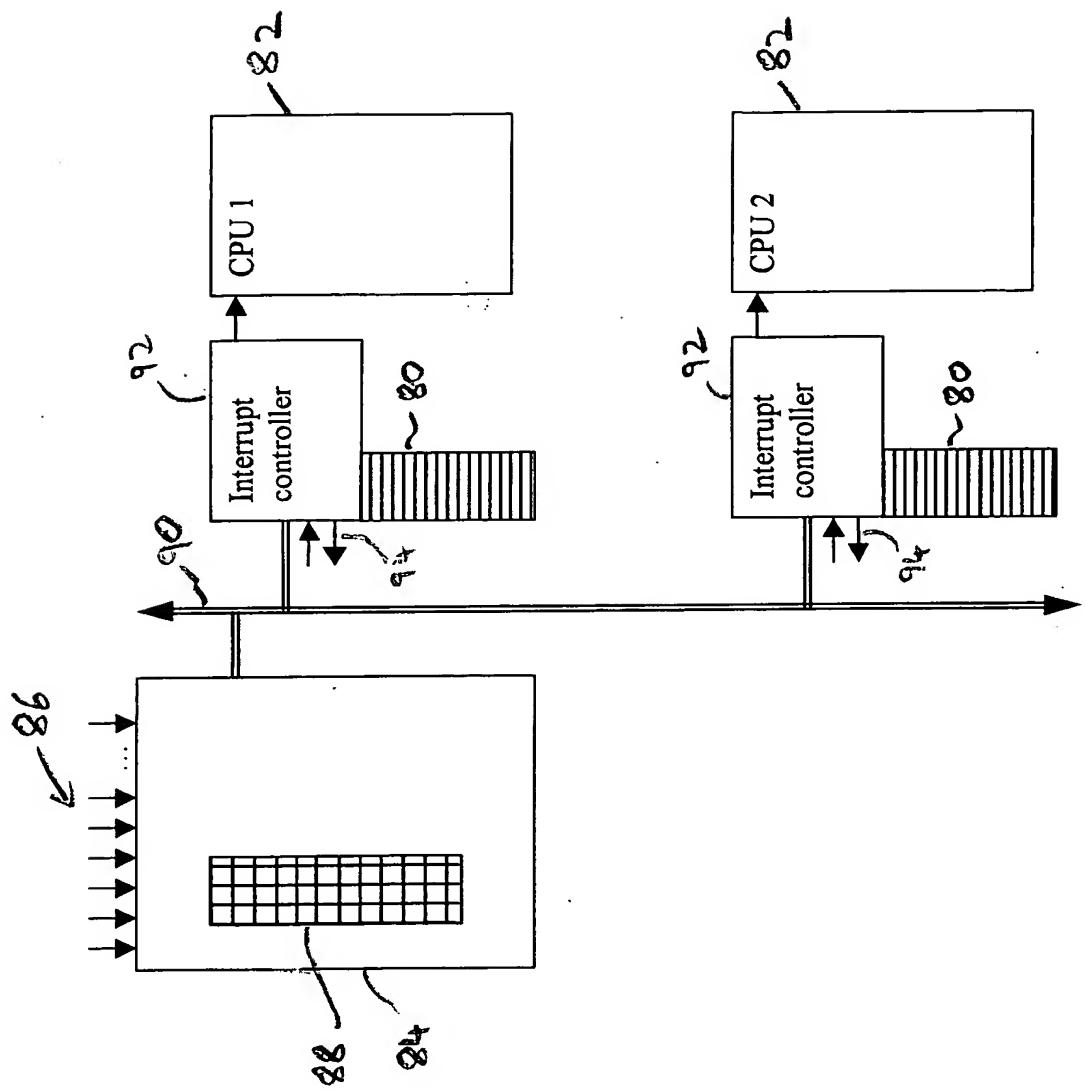


Fig. 5

Main Flow

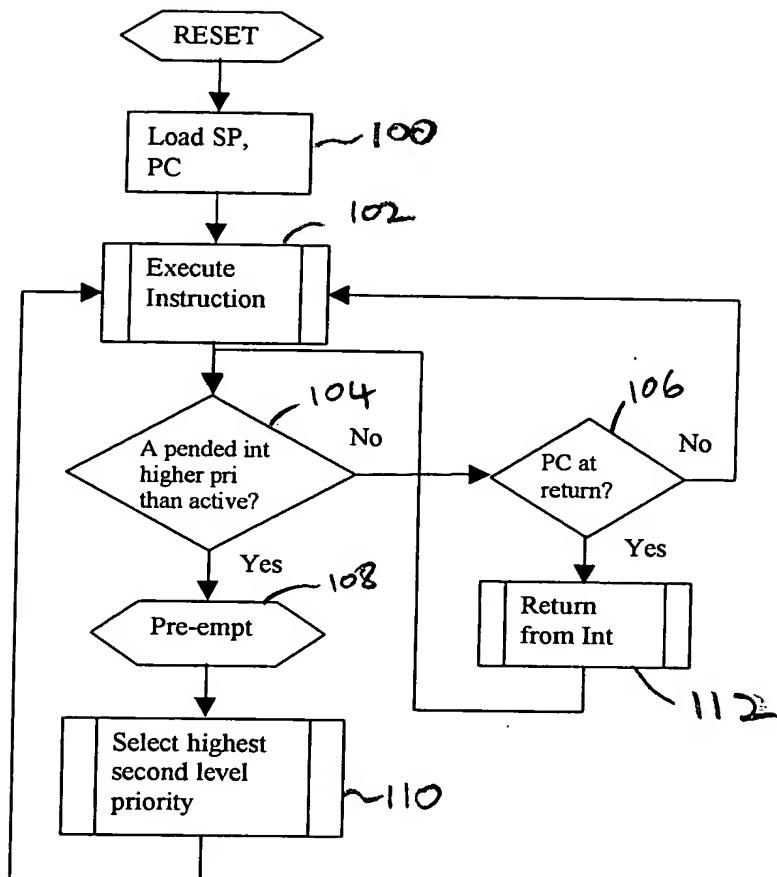


Fig. 6

Main Flow with Tail Chaining

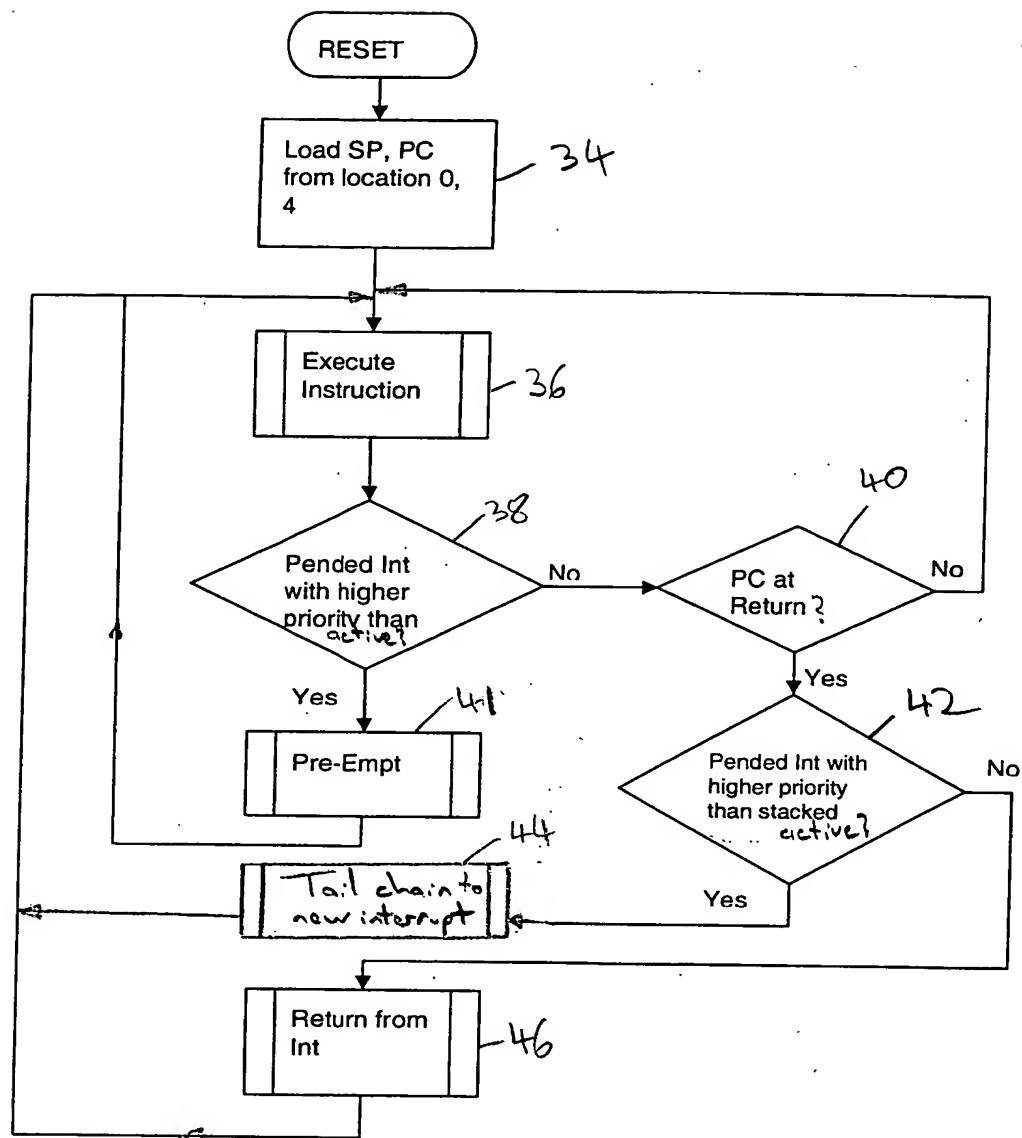


Fig. 7

Pre-emption Flow

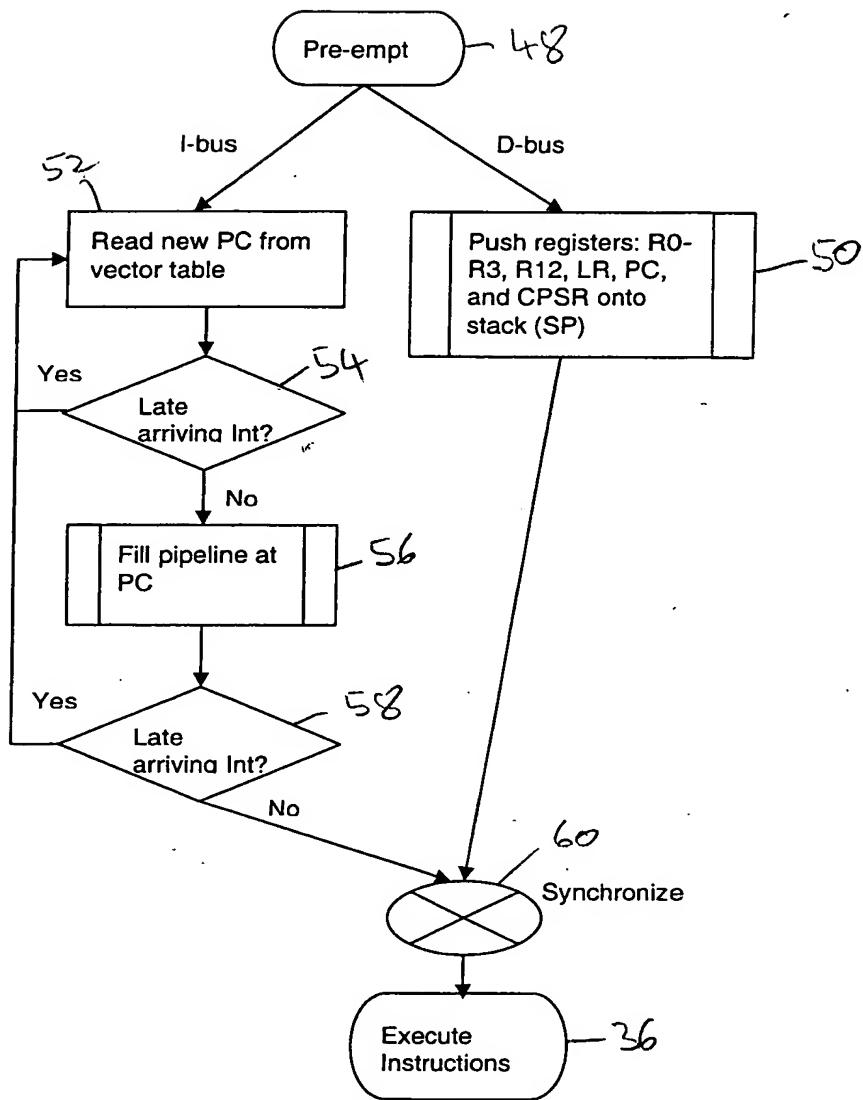


Fig. 8

Return From Interrupt Flow

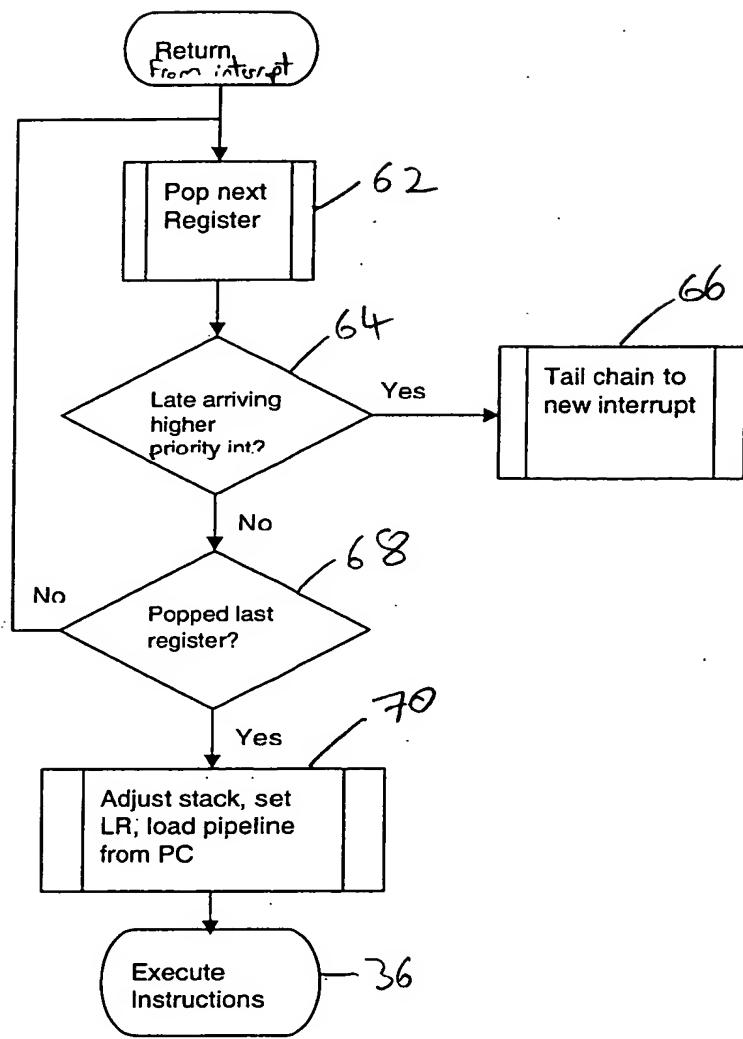
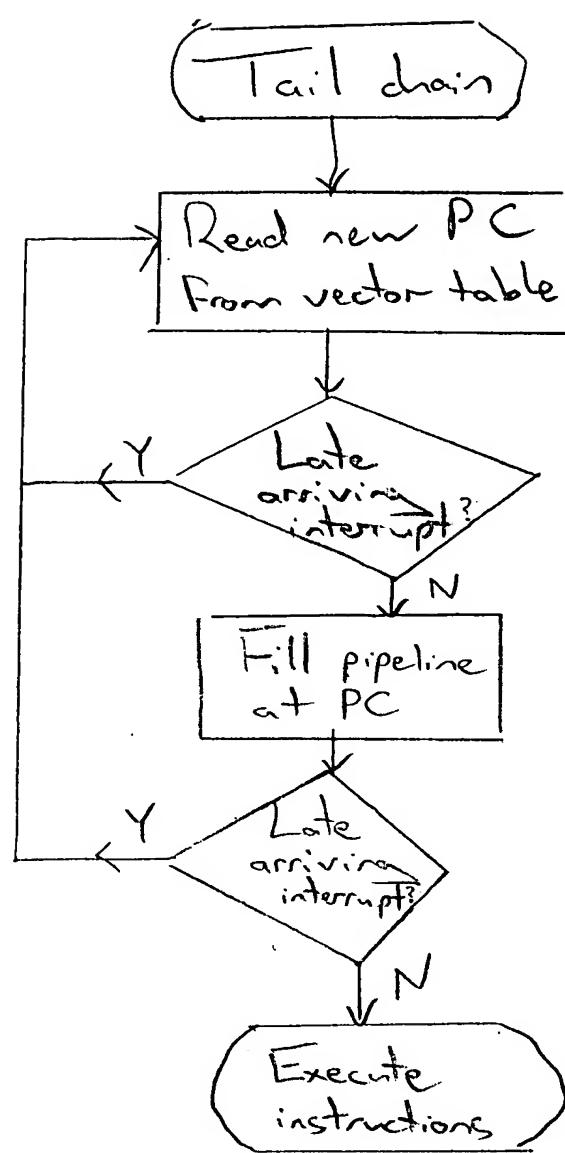


Fig. 9



Tail Chaining Flow

Fig. 10